T02T90*60867866

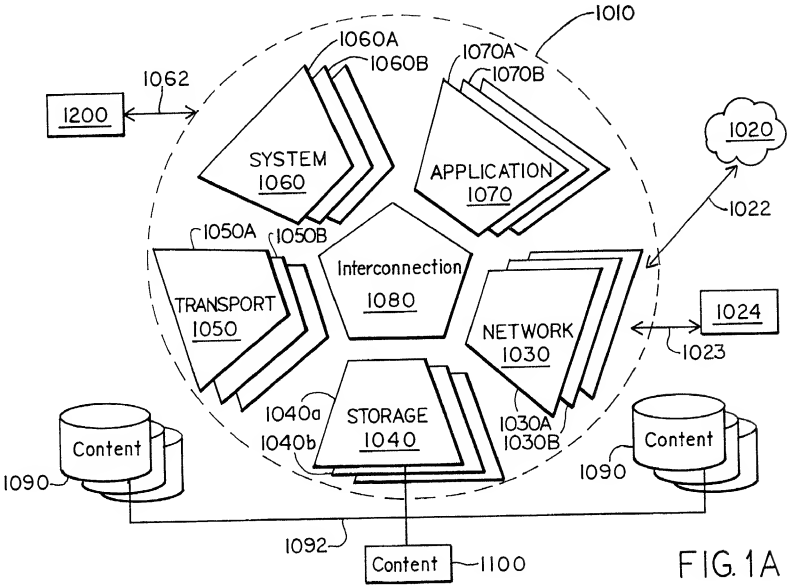


FIG. 1A

T02T90' 6088Z860

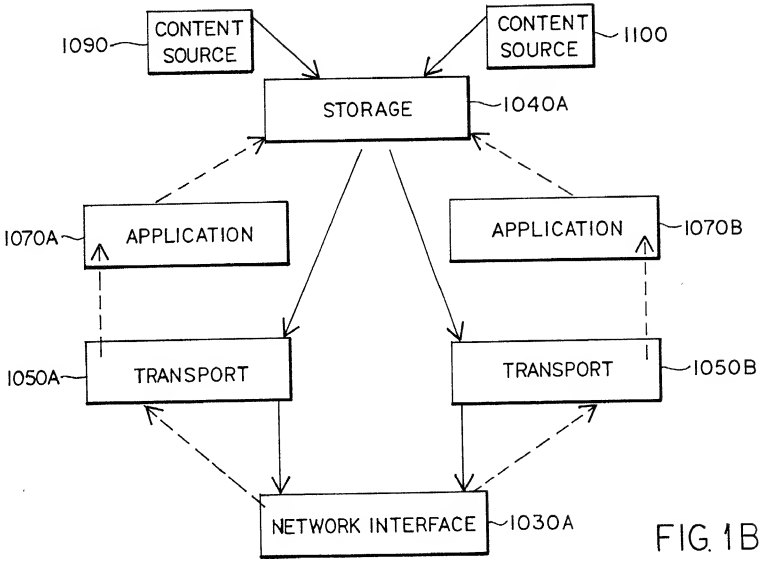


FIG. 1B

FIG.IC | FIG. IC' | FIG. IC''

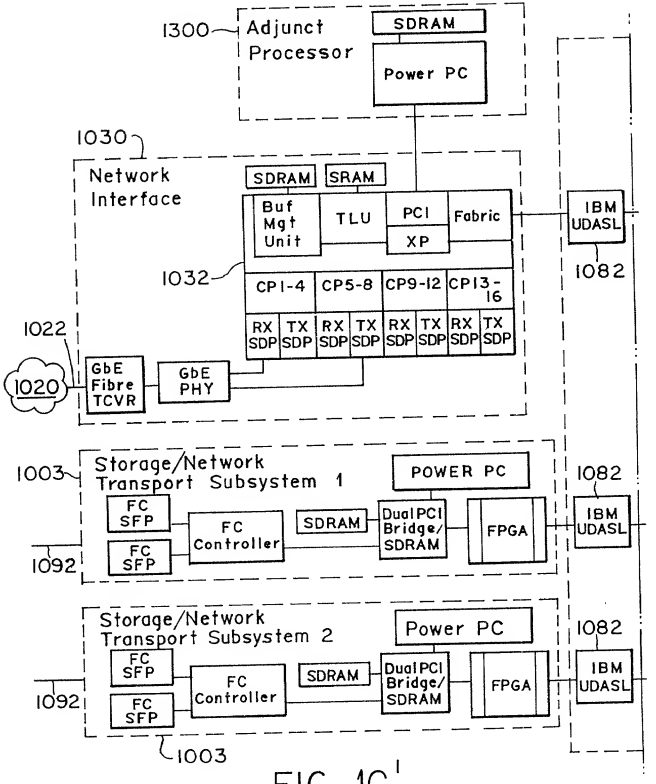


FIG. 1C'

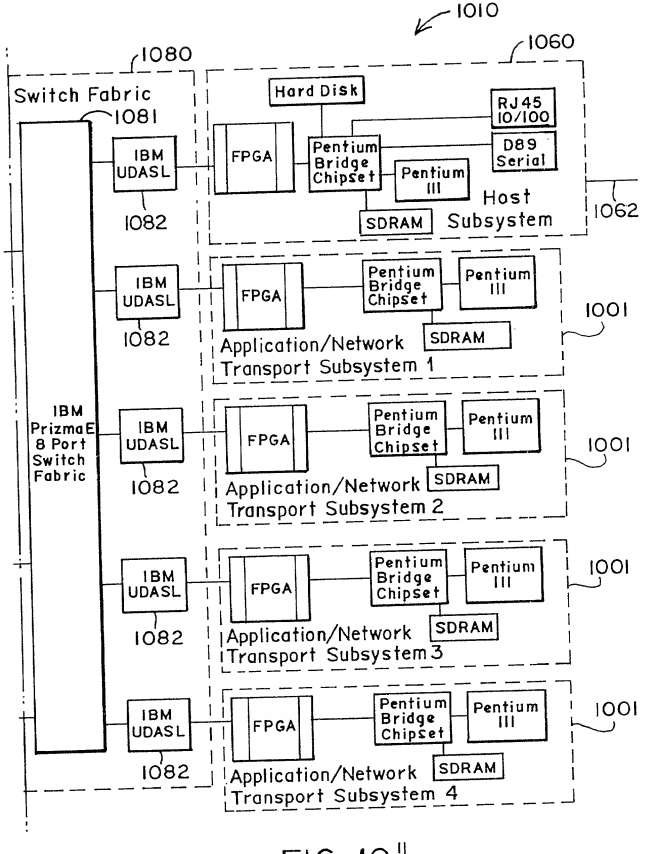

1010
1080
1060
Switch Fabric
1081
Hard Disk
RJ45 10/100
D89 Serial
IBM UDASL
FPGA
Pentium Bridge Chipset
Pentium III
SDRAM
Host Subsystem
1062
1082
Application/Network Transport Subsystem 1
1001
Application/Network Transport Subsystem 2
Application/Network Transport Subsystem 3
Application/Network Transport Subsystem 4
IBM PrizmaE 8 Port Switch Fabric

FIG. 1C[11]

FIG.ID | FIG. ID' | FIG. ID''

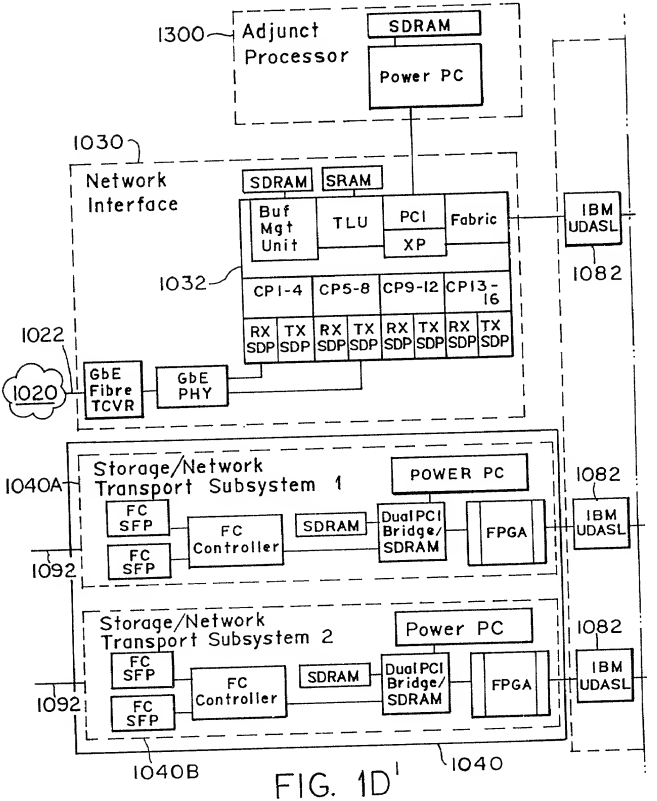


FIG. 1D'

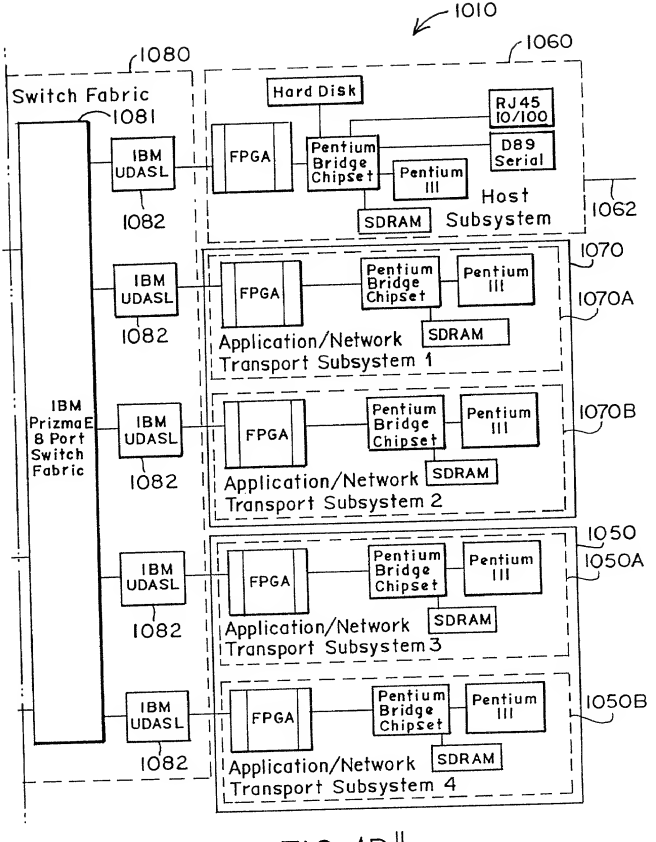

1010
1080
1060
Switch Fabric
1081
Hard Disk
RJ45 10/100
D89 Serial
FPGA
Pentium Bridge Chipset
Pentium III
SDRAM
Host Subsystem
1062
IBM UDASL
1082
1070
1070A
Application/Network Transport Subsystem 1
1070B
Application/Network Transport Subsystem 2
IBM Prizma E 8 Port Switch Fabric
1050
1050A
Application/Network Transport Subsystem 3
1050B
Application/Network Transport Subsystem 4

FIG. 1D[11]

FIG.1E | FIG.1E' | FIG.1E''

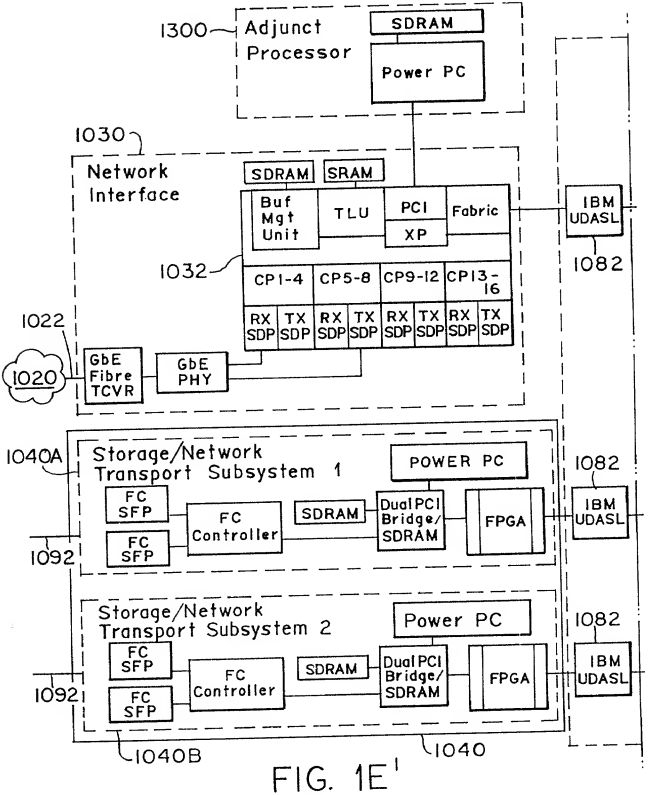


FIG. 1E'

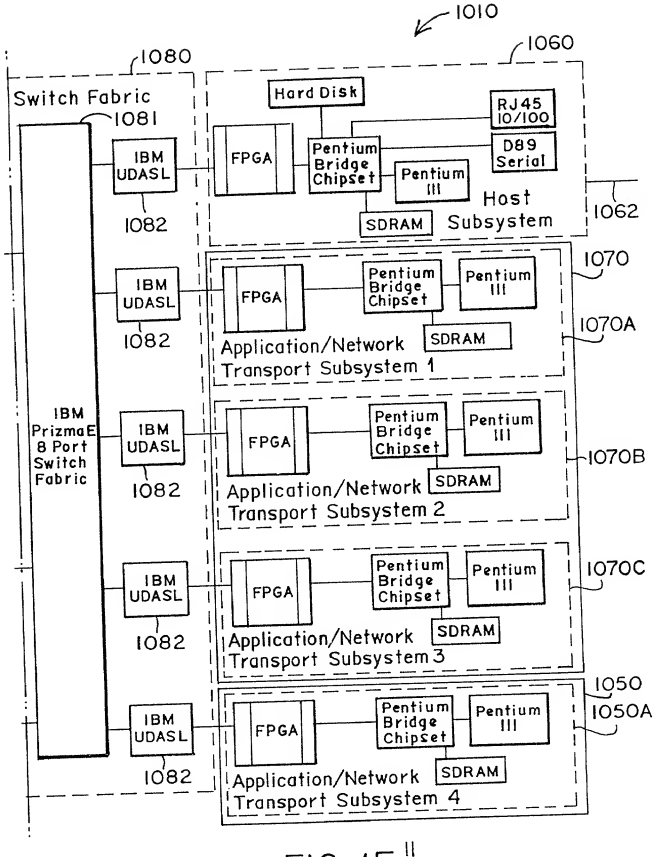

1010
1080
Switch Fabric
1081
IBM PrizmaE 8 Port Switch Fabric
IBM UDASL
1082
1060
Hard Disk
RJ45 10/100
FPGA
Pentium Bridge Chipset
D89 Serial
Pentium III
SDRAM
Host Subsystem
1062
1070
1070A
Application/Network Transport Subsystem 1
1070B
Application/Network Transport Subsystem 2
1070C
Application/Network Transport Subsystem 3
1050
1050A
Application/Network Transport Subsystem 4

FIG. 1E[11]

FIG.1F | FIG.1F' | FIG.1F''

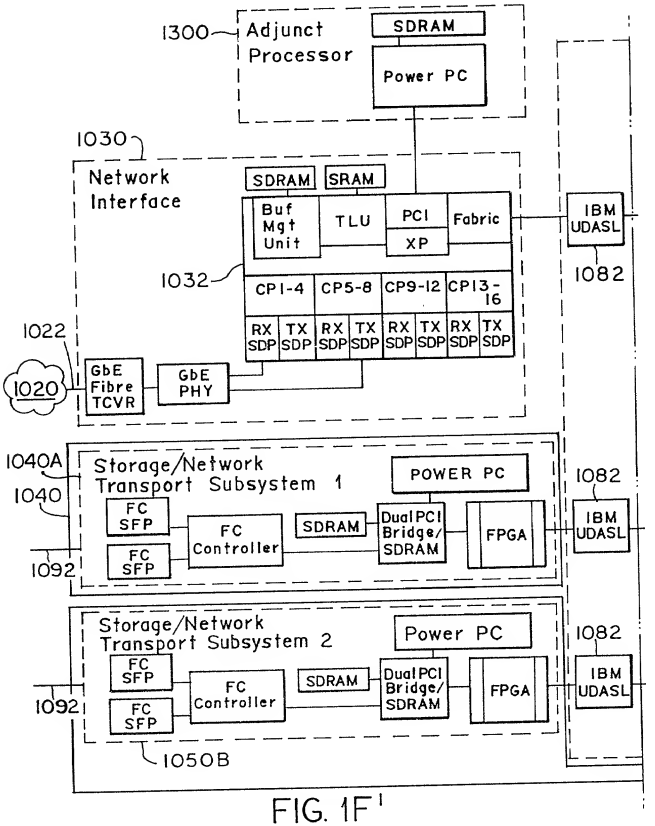


FIG. 1F'

T0E130 50866860

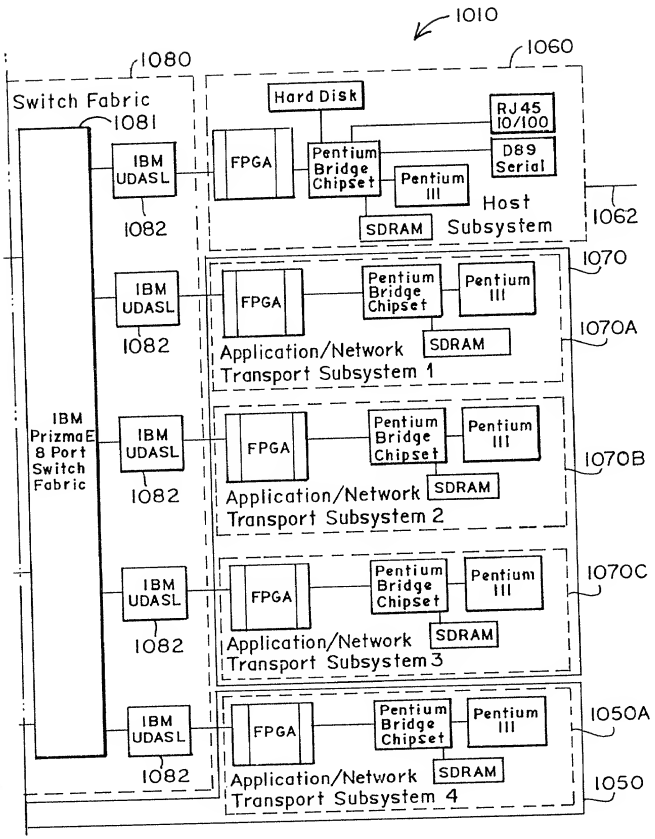


FIG. 1F II

T02T90'60867860

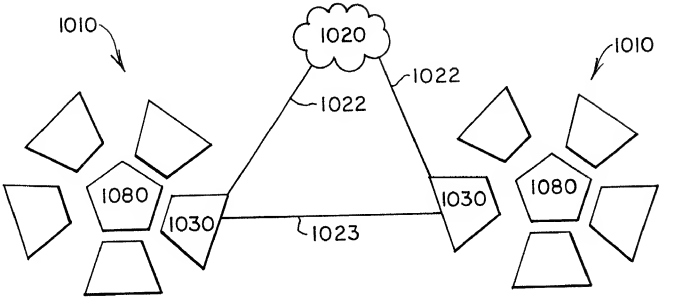


FIG. 1G

TO2790'6D86Z860

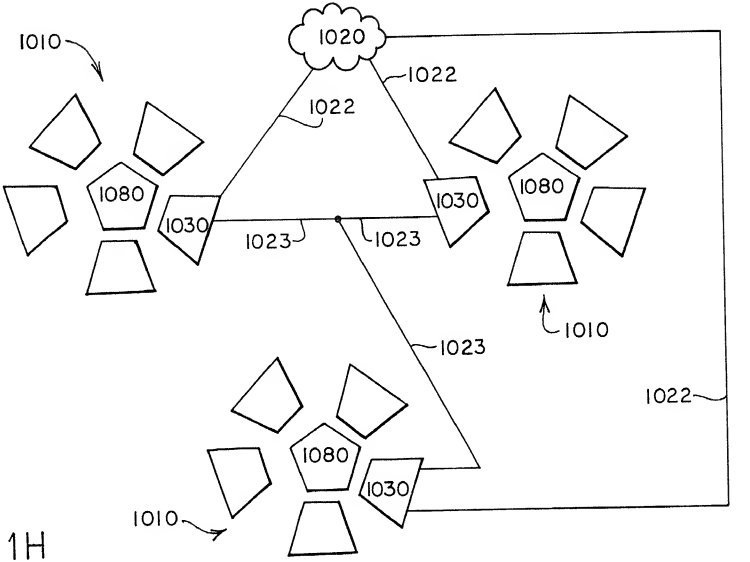


FIG. 1H

TD2T50'60B67860

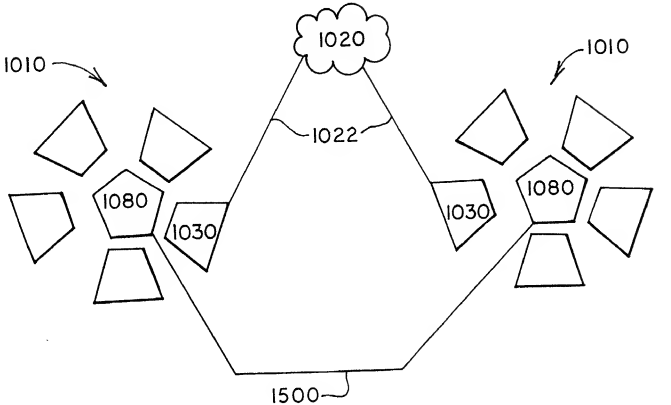


FIG. 1I

T02T90'60867980

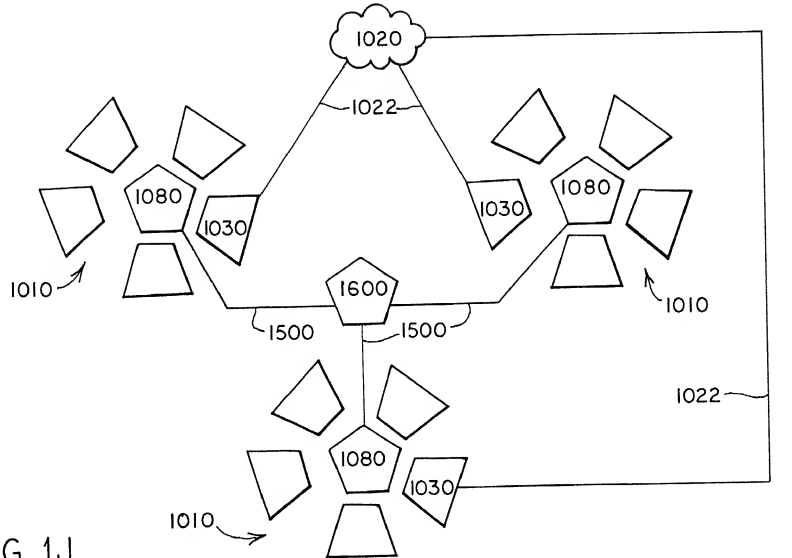


FIG. 1J

T021501 60868860

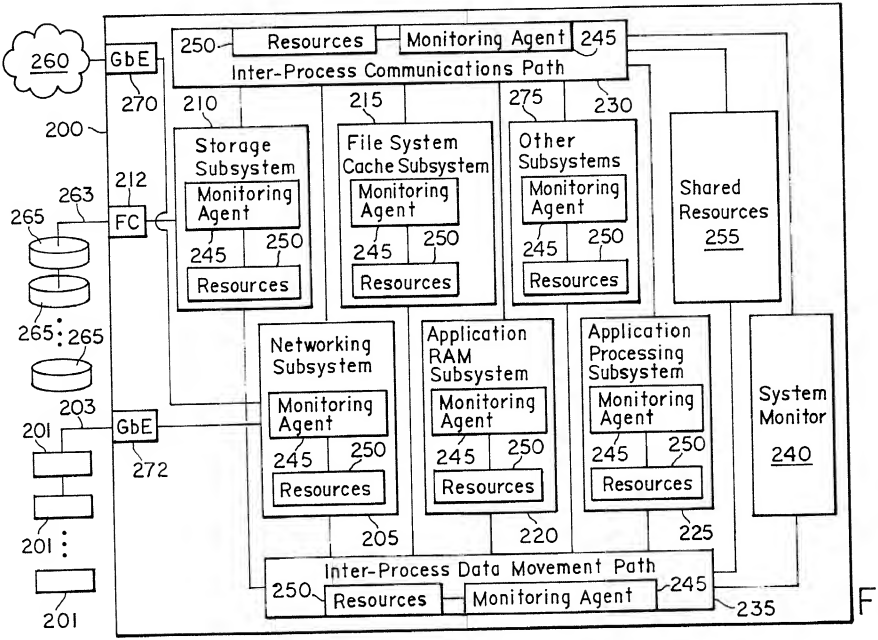


FIG.2

T02T90 60867860

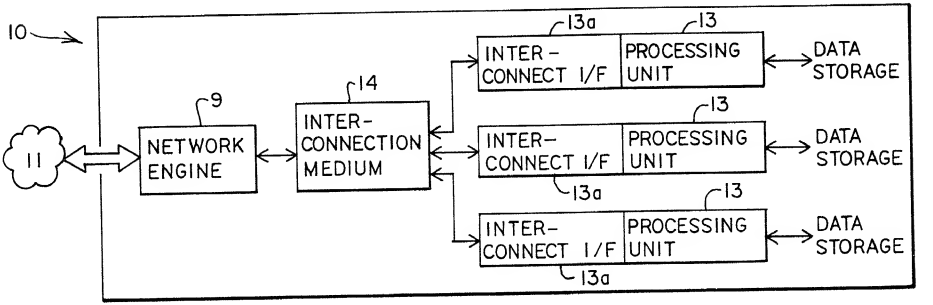


FIG. 2A

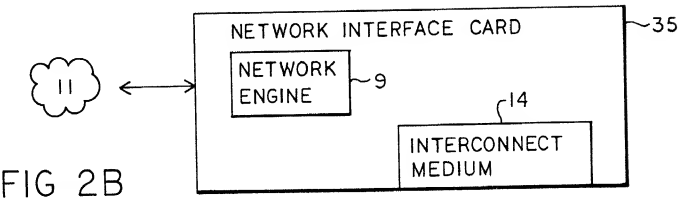


FIG 2B

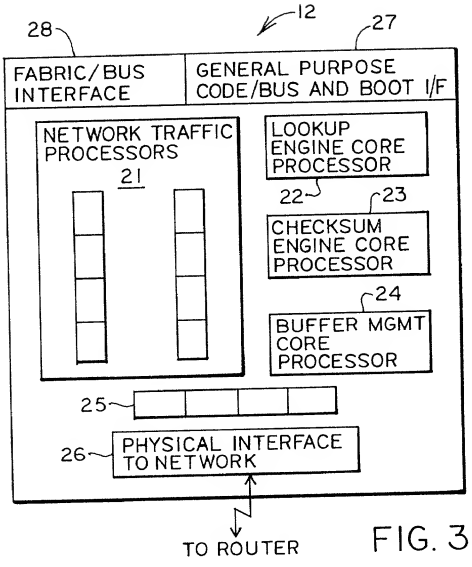

12
28
27
FABRIC/BUS INTERFACE
GENERAL PURPOSE CODE/BUS AND BOOT I/F
NETWORK TRAFFIC PROCESSORS
21
LOOKUP ENGINE CORE PROCESSOR
22
23
CHECKSUM ENGINE CORE PROCESSOR
24
BUFFER MGMT CORE PROCESSOR
25
26
PHYSICAL INTERFACE TO NETWORK
TO ROUTER

FIG. 3

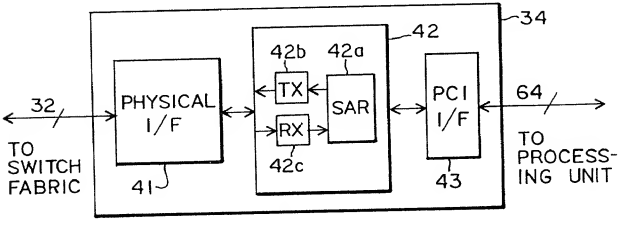

34
42
42b
42a
TX
RX
SAR
42c
32
PHYSICAL I/F
41
PCI I/F
43
64
TO SWITCH FABRIC
TO PROCESSING UNIT

FIG. 4

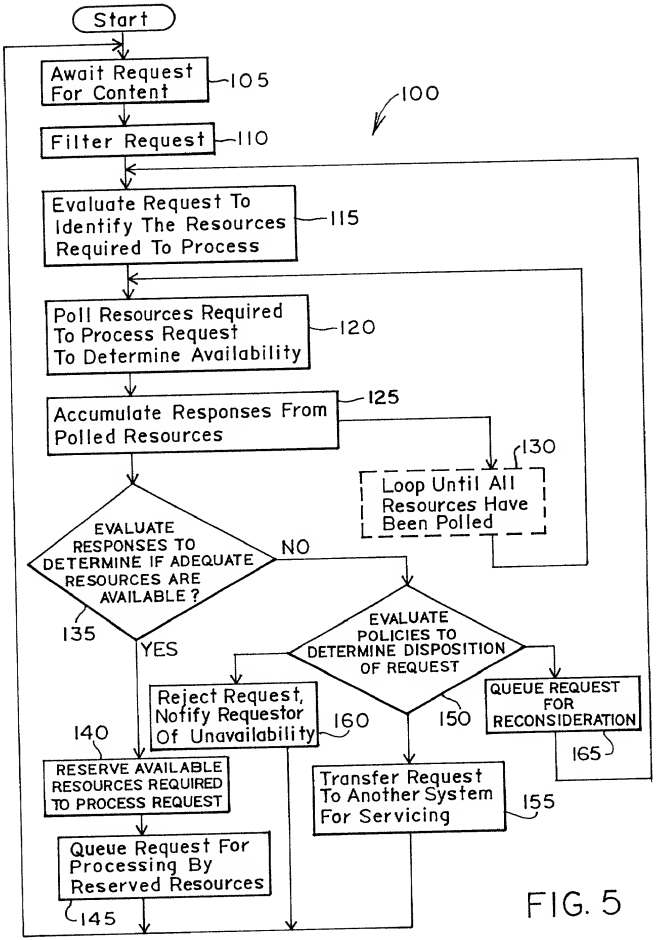

Start
Await Request For Content
105
100
Filter Request
110
Evaluate Request To Identify The Resources Required To Process
115
Poll Resources Required To Process Request To Determine Availability
120
Accumulate Responses From Polled Resources
125
130
Loop Until All Resources Have Been Polled
EVALUATE RESPONSES TO DETERMINE IF ADEQUATE RESOURCES ARE AVAILABLE?
NO
135
YES
EVALUATE POLICIES TO DETERMINE DISPOSITION OF REQUEST
150
Reject Request, Notify Requestor Of Unavailability
160
QUEUE REQUEST FOR RECONSIDERATION
165
140
RESERVE AVAILABLE RESOURCES REQUIRED TO PROCESS REQUEST
Transfer Request To Another System For Servicing
155
Queue Request For Processing By Reserved Resources
145

FIG. 5

FUZ1750 6066850

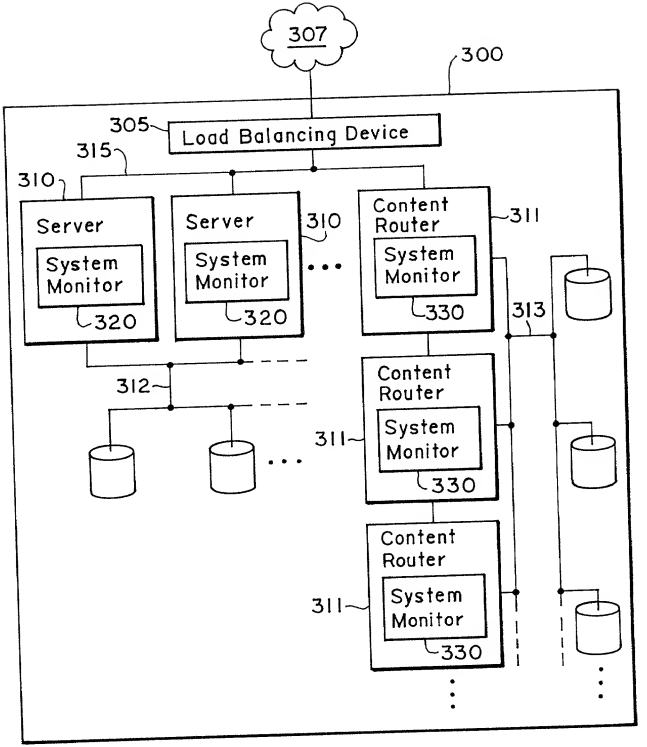
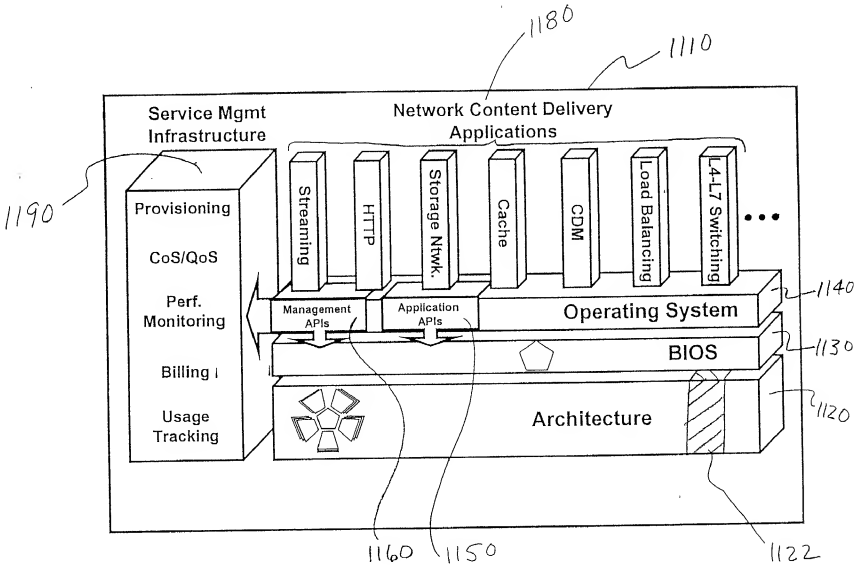


FIG. 6

TO2190 60867860



F16.7

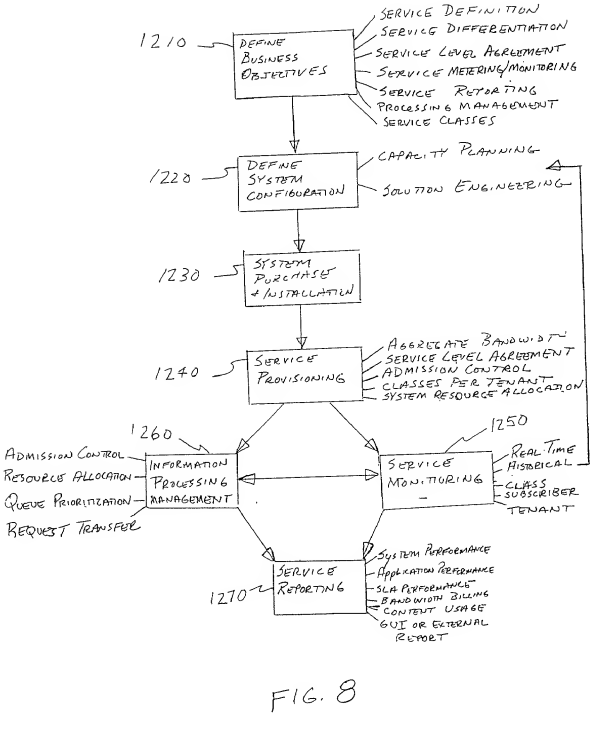

1210
DEFINE BUSINESS OBJECTIVES
SERVICE DEFINITION
SERVICE DIFFERENTIATION
SERVICE LEVEL AGREEMENT
SERVICE METERING/MONITORING
SERVICE REPORTING
PROCESSING MANAGEMENT
SERVICE CLASSES
1220
DEFINE SYSTEM CONFIGURATION
CAPACITY PLANNING
SOLUTION ENGINEERING
1230
SYSTEM PURCHASE & INSTALLATION
1240
SERVICE PROVISIONING
AGGREGATE BANDWIDTH
SERVICE LEVEL AGREEMENT
ADMISSION CONTROL
CLASSES PER TENANT
SYSTEM RESOURCE ALLOCATION
1260
ADMISSION CONTROL
RESOURCE ALLOCATION
QUEUE PRIORITIZATION
REQUEST TRANSFER
INFORMATION PROCESSING MANAGEMENT
1250
SERVICE MONITORING
REAL TIME
HISTORICAL
CLASS
SUBSCRIBER
TENANT
1270
SERVICE REPORTING
SYSTEM PERFORMANCE
APPLICATION PERFORMANCE
SLA PERFORMANCE
BANDWIDTH BILLING
CONTENT USAGE
GUI OR EXTERNAL REPORT

FIG. 8

T02T90' 60667860

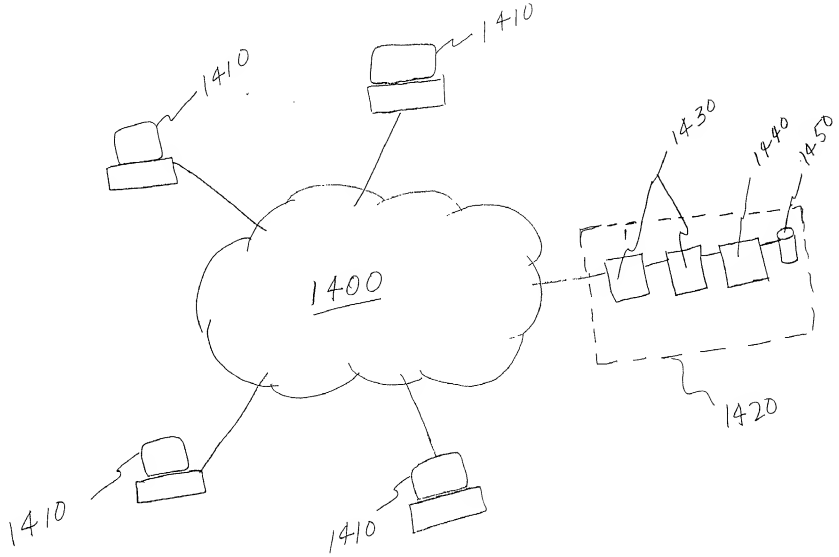


FIG. 9A

T02T90'60862850

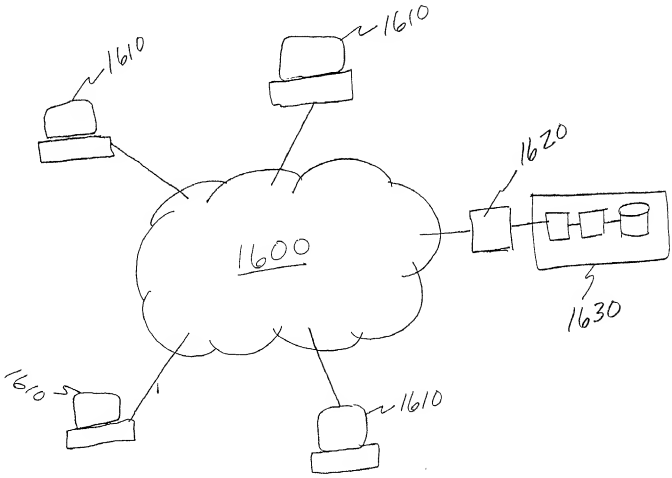


FIG. 9B

T02T30'60S67860

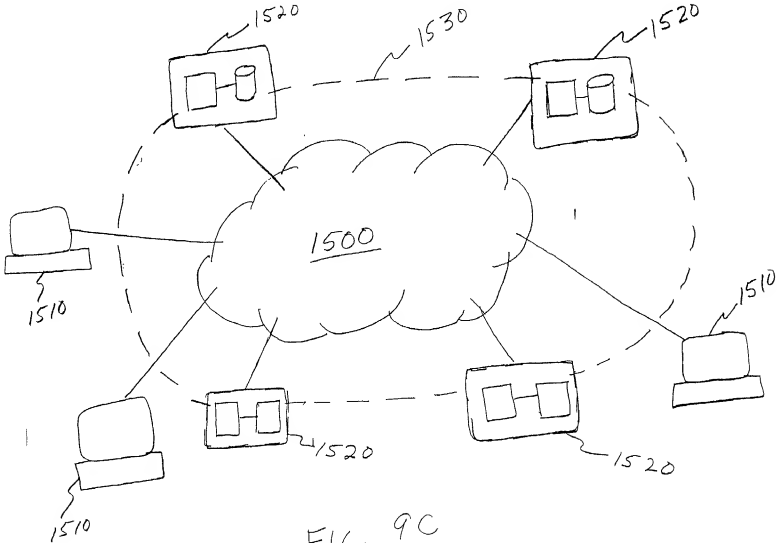


FIG. 9C

TOZT30'60562860

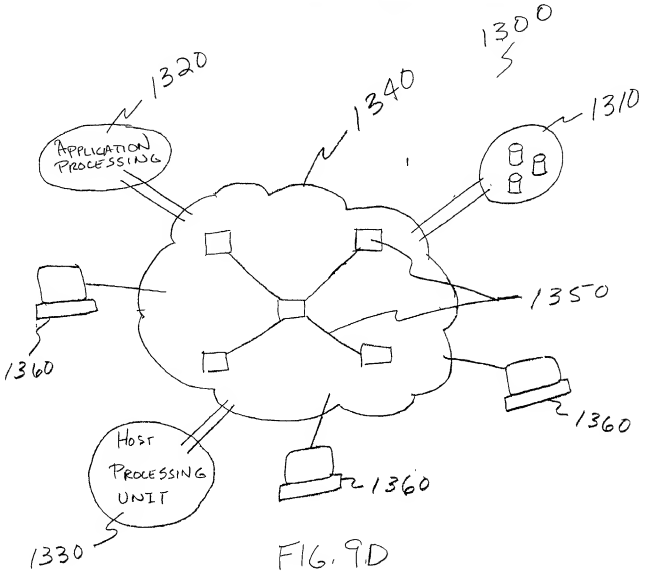


FIG. 9D